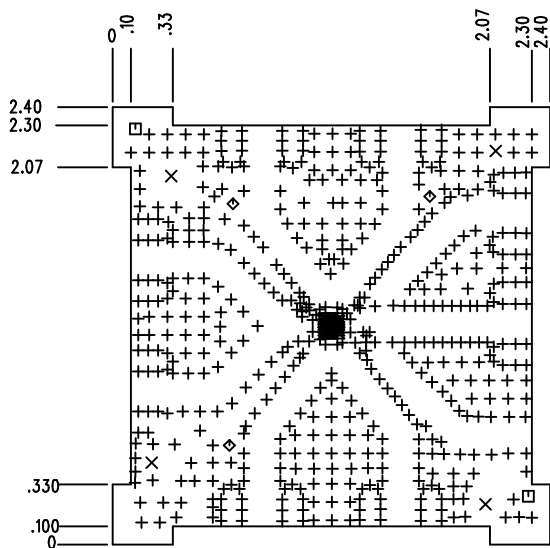
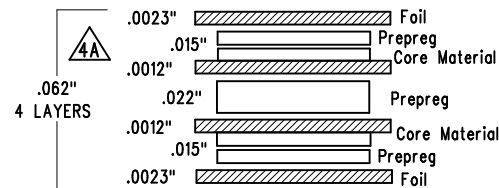


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
-	3	PRODUCTION	SUNNY H.	07-16-10



SIZE	QTY	SYM	PLATED	TOL
0.01	519	+	YES	+/-0.003
0.094	4	X	YES	+/-0.003
0.07	2	□	NO	+/-0.003
0.06614	3	◇	YES	+/-0.003
0.008	30	⊗	YES	+/-0.003

### LAYERING STRUCTURE

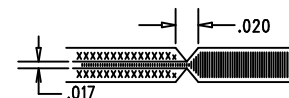


- NOTES :**
1. FAB PER IPC-A-600.
  2. MATERIAL: **Nelco N4000-13.**  
 -FINISHED THICKNESS TO BE 0.062" +/- .005"  
 -TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
 -FLAMMABILITY RATING: 94 V-0 MINIMUM.
  3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
 0.00" ARE PRIMARY DATUMS.
  4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
 -ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
 -HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
  5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
 -GOLD IMMERSION BOTH SIDES.  
 -FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.

CONTROLLED 50 OHM +/-5% IMPEDANCE FOR LAYER 1-2 and LAYER 4-3 TRACE WIDTH 30 MILS

**4A** SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.

6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		<b>APPROVALS</b>		1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
		PCB DES.	A. K.	<b>TECHNOLOGY</b>	
		APP ENG.	SUNNY H.	TITLE: FABRICATION DRAWING	
				HIGH LINEARITY DIRECT QUADRATURE MODULATOR	
				SIZE N/A	IC NO. LTC5588IPF DEMO CIRCUIT 1524A
					REV. 3
DO NOT SCALE DRAWING		SCALE: NONE		FILENAME: DC1524A-3.PCB	SHT 1 of 1